

SAMSUNG Component Ordering Information

<u>1</u> X	<u>2</u> X	<u>3</u> X	<u>4</u> XX	<u>5</u> XX	<u>6</u> X	<u>7</u> X	<u>8</u> X	<u>9</u> X	<u>10</u> X	<u>11</u> XX
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1 Samsung Memory :

DDR - K
DDR2 - K
DDR3 - K

2 DRAM

DDR - 4
DDR2 - 4
DDR3 - 4

3 Product

DDR	H - DDR SDRAM
DDR2	T - DDR2 SDRAM
DDR3	B - DDR3 SDRAM

4 Density & Refresh

DDR	28 - 128Mb, 4K/64ms
	56 - 256Mb, 8K/64ms
	51 - 512Mb, 8K/64ms
	1G - 1Gb, 8K/64ms
	2G - 2Gb, 8K/64ms
DDR2	56 - 256Mb 2G - 2Gb
	51 - 512Mb 4G - 4Gb
	1G - 1Gb
DDR3	51 - 512Mb 1G - 1Gb
	2G - 2GBb 4G - 4Gb

5 Organization

DDR	04 - x4
	06 - x4 Stack
	07 - x8 Stack
	08 - x8
	16 - x16
DDR2	04 - x4 06 - x4Stack
	07 - x8Stack 08 - x8
	16 - x16 26 - x4Stack
	27 - x8Stack(JEDEC Standard /26)
DDR3	04 - x4 08 - x8
	16 - x16

8 Revision

DDR	M - 1st Gen	A - 2nd Gen
	B - 3rd Gen	C - 4th Gen
	D - 5th Gen	E - 6th Gen
	F - 7th Gen	G - 8th Gen
	H - 9th Gen	J - 11th Gen
	N - 14th Gen	
DDR2	M - 1st Gen	A - 2nd Gen
	B - 3rd Gen	C - 4th Gen
	D - 5th Gen	E - 6th Gen
	F - 7th Gen	G - 8th Gen
	H - 9th Gen	Q - 17th Gen
	R - 18th Gen	
DDR3	M - 1st Gen	A - 2nd Gen
	B - 3rd Gen	C - 4th Gen
	D - 5th Gen	E - 6th Gen
	F - 7th Gen	G - 8th Gen
	H - 9th Gen	

9 Package Type

DDR	T - TSOP II	U - TSOP II (L/F)
	N - STSOP II	V - STSOP II (L/F)
	G - FBGA	Z - FBGA (L/F)
	L - TSOP II (L/F & H/F)	
	H - FBGA (L/F & H/F)	
	F - FBGA for 64Mb DDR (L/F & H/F)	
	6 - STSOP II (L/F & H/F)	
	* L/F - Lead-Free , H/F - Halogen-Free	
DDR2	Z - FBGA (Lead-Free)	
	J - FBGA DDP (Lead-Free)	
	H - FBGA (Lead-Free & Halogen-Free)	
	M - FBGA DDP (L/F & H/F)	
	T - FBGA DSP (L/F & H/F ,Thin)	
DDR3	Z - FBGA (Lead-Free)	
	H - FBGA (Lead-Free & Halogen-Free)	
	J - FBGA (Lead-Free , DDP)	
	M - FBGA (Lead-Free & H/F , DDP)	

6 Bank

DDR	3 - 4 Bank	DDR3	3 - 4Bank
DDR2	3 - 4 Bank		4 - 8Bank
	4 - 8 Bank		5 - 16Bank

7 Interface (VDD, VDDQ)

DDR	8 - SSTL-2 (2.5V,2.5V)
DDR2	Q - SSTL-1.8V(1.8V,1.8V)
DDR3	6 - SSTL (1.5V,1.5V)

10 Temperature & Power

DDR	C - Commercial Temp (0~70) & N/P
	L - Commercial Temp (0~70) & L/P
	I - Industrial Temp(-40~85) & N/P
	P - Industrial Temp(-40~85) & L/P
DDR2	C - Commercial Temp (0~95) & N/P
	L - Commercial Temp (0~95) & L/P
	Y - Commercial Temp (0~95) & N/V
DDR3	C - Commercial Temp(0~85) & N/P
	L - Commercial Temp(0~85) & L/P
	Y - Commercial Temp(0~85) & Low vdd

11 Speed

DDR	CC - DDR400 (200MHz@CL=3)
	B3 - DDR333 (166MHz@CL=2.5)
	A2 - DDR266 (133MHz@CL=2)
	B0 - DDR266 (133MHz@CL=2.5)
DDR2	CC - DDR2-400 (200MHz@CL=3)
	D5 - DDR2-533 (266MHz@CL=4)
	E6 - DDR2-667 (333MHz@CL=5)
	F7 - DDR2-800 (400MHz@CL=6)
	E7 - DDR2-800 (400MHz@CL=5)
	F8 - DDR2-1066 (533MHz@CL=7)
DDR3	F7 - DDR3-800(400MHz@CL=6)
	F8 - DDR3-1066(533MHz@CL=7)
	H9 - DDR3-1333(400MHz@CL=9)
	K0 - DDR3-1600(400MHz@CL=11)

